

# On Power Factor Improvement by Lossless Linear Filters under Nonlinear Nonsinusoidal Conditions

**Abstract.** Recently, it has been established that the problem of power factor compensation for nonlinear loads with nonsinusoidal source voltage can be recast in terms of the property of cyclodissipativity. The purpose of this brief note is to review and to illustrate the application of this framework to the practical of passive reactive compensation of linear loads with a nonsinusoidal source voltage. We give criteria for determining the optimal values of the compensator parameters that depend of the spectral line of load susceptance and voltage source.

**Streszczenie.** Stwierdzono ostatnio, że problem poprawy współczynnika mocy odbiorników nieliniowych zasilanych napięciem niesinusoidalnym może być rozpatrywany z punktu widzenia cyklo-rozpraszania (cyclodissipativity). Celem tego artykułu jest przegląd i pokazanie zastosowania takiego podejścia przy bezstratnej kompensacji odbiorników liniowych zasilanych napięciem niesinusoidalnym. Podane zostały kryteria do określenia optymalnych wartości parametrów kompensatora, które zależą od charakterystyki widmowej susceptancji obciążenie i źródła napięcia. (**Poprawa współczynnika mocy filtrami bezstratnymi w warunkach nieliniowych i niesinusoidalnych**)

**Keywords:** Power factor compensation; nonlinear network; nonsinusoidal conditions.

**Słowa kluczowe:** Kompensacji współczynnika mocy; sieć nieliniowych; warunkach niesinusoidalnych.

## Introduction

Optimizing energy transfer from an alternating current (ac) source to a load is a classical problem in electrical engineering. In practice, the efficiency of this transfer is typically reduced due to the phase shift between voltage and current at the fundamental frequency. The power factor captures the energy-transmission efficiency for a given load. The standard approach to improving the power factor is to place a compensator between the source and the load.

The effectiveness of capacitive compensation in systems with nonsinusoidal voltages and currents has been widely studied by [1, 2] and [3]. Unfortunately, in [4] it has been illustrated that the capacitive compensation may not be effective for non-sinusoidal voltages. Therefore, a more complex compensator than only a capacitor is required for the reactive power minimization in such situations.

Recently, in [5] it has been established that the classical problem is equivalent to imposing the property of cyclodissipativity to the source terminals. Since this framework is based on the cyclodissipativity property, see [6, 7], the improvement of the power factor (PF) is done independent of the reactive power definition, which is a matter of discussions in the power community, see for instance [8] and its references. Most of the approaches used to improve the PF are based on different power definitions and a lack of a unified definition of reactive power produces misunderstanding of power phenomena in circuits with nonsinusoidal voltages and currents, [9]. Similarly, the task of designing compensators that aim at improving the PF for nonlinear time-varying loads operating in non-sinusoidal regimes is far from clear.

Using the cyclodissipativity framework the classical capacitor and inductor compensators were interpreted in terms of energy equalization, see [5] for more details. And we have presented an extension of this result in [10] where we considered arbitrary lossless linear time invariant (LTI) filters, and proved that for general lossless LTI filters the PF is reduced if and only if a certain equalization condition between the weighted powers of inductors and capacitors of the load is ensured.

Here we illustrate the application of this framework to the passive compensation of linear loads with a non-sinusoidal source voltage. We give criteria for improvement of PF with linear capacitors, LC filters, which determine the optimal values for the compensator parameters that depend of the spectral line of load susceptance and voltage source.

## Cyclodissipativity of RLC nonlinear networks

In order to make this paper self-contained, the purpose of this section is to briefly review the meaning of cyclodissipativity, [6, 7], and some of its connections with the nonlinear circuit theory. Although the dissipativity theory applies to a wider classes of systems, let us consider dynamical systems modeled by ordinary differential equations:

**Definition 1** (Input-State-Output Representation). *The input-state-output representation of the dynamical systems  $\mathcal{H} : \mathcal{U} \rightarrow \mathcal{Y}$ , is of the form*

$$(1) \quad \begin{aligned} \dot{x} &= f(x, u), & x &\in \mathcal{X} \subset \mathbb{R}^m \\ y &= g(x, u), & u &\in \mathcal{U} \subset \mathbb{R}^n, y \in \mathcal{Y} \subset \mathbb{R}^n, \end{aligned}$$

where  $f: \mathcal{X} \times \mathcal{U} \rightarrow \mathbb{R}^n$  and  $g: \mathcal{X} \times \mathcal{U} \rightarrow \mathcal{Y}$  are vector functions of class  $C^k$ , with  $0 < k < \infty$ . Let  $\mathcal{X}$  be the set of reachable and controllable points. Furthermore, assume that system (1), for  $u = 0$ , has an equilibrium point in  $x = 0$ . That is,  $f(0, 0) = 0$  and  $g(0, 0) = 0$ .

The definition of cyclodissipativity involves a function called *supply rate*  $w: \mathcal{U} \times \mathcal{Y} \rightarrow \mathbb{R}$ , which is locally integrable for every  $u \in \mathcal{U}$ , [6].

**Definition 2** (Cyclodissipativity). *We say that the system  $\mathcal{H}$  is cyclodissipative on  $\mathcal{X}$  with the supply rate  $w(u, y)$  if there exists a function  $S$ , called storage function, such that*

$$(2) \quad S(x(t_0)) + \int_{t_0}^{t_1} w(u(t), y(t)) dt \geq S(x(t_1)),$$

is satisfied for all  $u \in \mathcal{U}$  and all  $t_0 < t_1$ , such that  $x(t) \in \mathcal{X}$  for all  $t \in [t_0, t_1]$ .

The inequality (2) is similar to the usual dissipation inequality where additionally it is required that  $S \geq 0$ , and which expresses that the increase in energy stored cannot be larger than the energy supplied from the outside. Furthermore, if the system  $\mathcal{H}$  does not produce energy at any time, namely that the energy stored from the system is finite,  $S(x) \geq 0$  for all  $x \in \mathcal{X}$ , then the system is called dissipative. Typical examples of dissipative systems are: passive electrical networks, mechanical systems, viscoelastic materials, etc.

However, if we consider an electrical network with active elements, namely negative resistors, tunnel diodes, etc., then the interpretation of (2) leads to some difficulties because the storage energy function needs in general not be bounded from below or above. This merely involved a generalization of the concept of a dissipative system to that of

a cyclo-dissipative system<sup>1</sup>. The concept of cyclo-dissipativity is inspired by the fact that cyclo-dissipative systems exhibit a dissipative behavior in cyclic motions. However, normally, autonomous systems cannot exhibit periodic (or recurrent) motions. Hence, we restrict the set of inputs of interest to those inputs that generate periodic trajectories, and we consequently define the following signal space.

**Definition 3.** Let  $\mathcal{L}_2^n$  be the signal space defined by  $\mathcal{L}_2^n := \left\{ x : [0, T] \rightarrow \mathbb{R}^n : \|x\|^2 := \frac{1}{T} \int_0^T |x(\tau)|^2 d\tau < \infty \right\}$ , where  $\|\cdot\|$  is the rms value,  $|\cdot|$  is the Euclidean norm and the inner product in  $\mathcal{L}_2^n$  is defined as  $\langle x, y \rangle := \frac{1}{T} \int_0^T x^\top(t)y(t)dt$ .

We thus arrive at the following lemma:

**Lemma 1** (Cyclo-dissipativity). Given a mapping  $w : \mathcal{L}_2^n \times \mathcal{L}_2^n \rightarrow \mathbb{R}$ . The system  $\mathcal{H}$  is cyclo-dissipative with respect to the supply rate  $w(u, y)$  if and only if

$$(3) \quad \int_0^T w(t)dt \geq 0.$$

for all  $T \geq 0$ , and  $u \in \mathcal{L}_2^n$  whenever  $x(0) = x(T)$ .

**Remark 1.** In words, a system is cyclo-dissipative when it can not create (abstract) energy over closed paths in the state-space. It might, however, produce energy along some initial portion of such a trajectory; if so, it is only cyclo-dissipative and not dissipative.

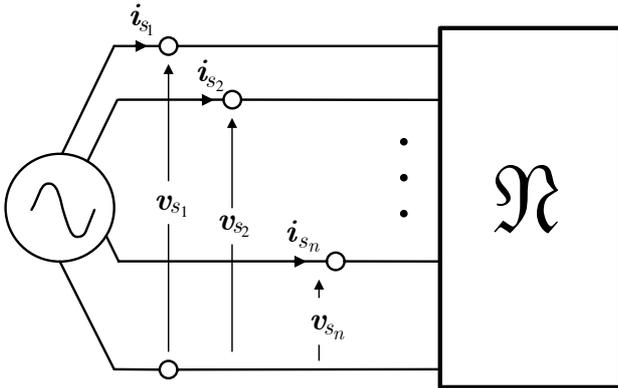


Fig. 1. Illustrating power delivered to a (possibly nonlinear and time varying) load from an  $n$ -phase ac ideal generator.

For instance, consider the  $n$ -port network  $\mathfrak{N}$  of Figure 1 consists of arbitrary interconnection of  $n_L$  inductors,  $n_C$  capacitors with  $n_R$  passive resistors. From the principle of conservation of energy, regardless of the nature of the elements or the excitation, the instantaneous rate of energy transfer or instantaneous power at the input terminal is equal to

$$\sum_{p=1}^n i_{s_p} v_{s_p} = \sum_{\alpha=1}^{n_L} i_{L_\alpha} v_{L_\alpha} + \sum_{\beta=1}^{n_C} i_{C_\beta} v_{C_\beta} + \sum_{\gamma=1}^{n_R} i_{R_\gamma} v_{R_\gamma},$$

where we have adopted the standard sign convention for the (instantaneous) supplied power  $i_s^\top v_s$ , which can be rewritten as,

$$(4) \quad \sum_{p=1}^n i_{s_p} v_{s_p} = \frac{d\mathcal{E}}{dt} + i_R^\top v_R,$$

<sup>1</sup>As explained in [6], cyclo-dissipativity is understood here in terms of the available generalized energy. The idea is borrowed from thermodynamics, where the notion is formulated in a conceptually clearer manner than in circuits and systems theory.

where

$$(5) \quad \mathcal{E}(t) := \sum_{\alpha=1}^{n_L} \mathcal{E}_{L_\alpha}(t) + \sum_{\beta=1}^{n_C} \mathcal{E}_{C_\beta}(t)$$

represents the total stored energy in the network. Time-integration of (4) over the interval  $[t_0, t_1]$  yields

$$(6) \quad \int_{t_0}^{t_1} i_s^\top(\tau)v_s(\tau)d\tau = \mathcal{E}(t_1) - \mathcal{E}(t_0) + \int_{t_0}^{t_1} i_R^\top(\tau)v_R(\tau)d\tau.$$

Let us assume that all resistors are passive, namely the instantaneous dissipated power is always positive  $i_R^\top(t)v_R(t) \geq 0$  for all  $t \in [t_0, t_1]$ , then we obtain the dissipation inequality,

$$(7) \quad \mathcal{E}(t_0) + \int_{t_0}^{t_1} i_s^\top(\tau)v_s(\tau)d\tau \geq \mathcal{E}(t_1),$$

which verifies for all  $t_1 \geq t_0$ . Indeed, we can conclude the following result.

**Proposition 2.** A  $n$ -port network  $\mathfrak{N}$  consists of arbitrary interconnection of  $n_L$  inductors,  $n_C$  capacitors with  $n_R$  passive resistors verify the dissipation inequality (7) for all  $t_1 \geq t_0$  and, therefore,  $\mathfrak{N}$  is cyclo-dissipative with respect to the supply rate  $w(u_s, i_s) = i_s^\top(\cdot)v_s(\cdot)$  and the storage function is the total stored energy (5).

### A cyclo-dissipativity characterization of power factor compensation

This section introduces the identification of the key role played by cyclo-dissipativity in power factor compensation.

We consider the energy transfer from an  $n$ -phase ac generator to a load, see Figure 1. The voltage and current of the source are denoted by the column vectors  $v_s(t), i_s(t) \in \mathbb{R}^n$  and the load is described by a (possibly nonlinear and time varying)  $n$ -port network  $\mathfrak{N}$ . We make the following assumption.

**Assumption 1.** All signals are assumed to be periodic,  $x(t) = x(t+T)$ , and have finite power, that is, they belong to  $\mathcal{L}_2^n$ .

**Assumption 2.** The source is ideal<sup>2</sup>, in the sense that  $v_s$  remains unchanged for all loads  $Y_\ell$ .

The universally accepted definition of PF is given as [11]:

**Definition 4** (Power factor). The PF of the source is defined by

$$(8) \quad PF := \frac{P}{S},$$

where

$$(9) \quad P := \langle v_s, i_s \rangle,$$

is the active real power, also equal to average power [12], and  $S := \|v_s\| \|i_s\|$  is the apparent power.

From (8) and the Cauchy–Schwartz inequality, it follows that  $P \leq S$ . Hence  $PF \in [-1, 1]$  is a dimensionless measure of the energy-transmission efficiency. Cauchy–Schwartz also tells us that a necessary and sufficient condition for the apparent power to equal the active power is that  $v_s$  and  $i_s$  are collinear. If this is not the case,  $P < S$  and compensation schemes are introduced to maximize the PF.

<sup>2</sup>Under Assumption 2, the apparent power  $S$  is the highest average power delivered to the load among all loads that have the same rms current  $\|i_s\|$ .

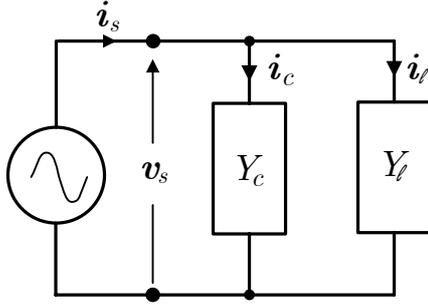


Fig. 2. Schematic diagram of shunt PF compensation configuration.

The PF compensation configuration considered in the paper is depicted in Figure 2, where  $Y_c, Y_\ell : \mathcal{L}_2^n \rightarrow \mathcal{L}_2^n$  are the admittance operators of the compensator and the load, respectively. That is,  $i_c = Y_c(v_s)$ ,  $i_\ell = Y_\ell(v_s)$  where  $i_c, i_\ell \in \mathcal{L}_2^n$ , are the compensator and load currents, respectively. In the simplest LTI case the operators  $Y_c, Y_\ell$  can be described by their admittance transfer matrices, which we denote by  $\hat{Y}_c(s), \hat{Y}_\ell(s) \in \mathbb{R}^{n \times n}(s)$ , respectively, where  $s$  represents the complex frequency variable  $s = j\omega$ .

The uncompensated PF, that is, the value of  $PF$  when  $Y_c = 0$ , is clearly given by

$$(10) \quad PF_u := \frac{\langle v_s, i_\ell \rangle}{\|v_s\| \|i_\ell\|},$$

where  $i_s = i_\ell$ .

**Definition 5** (Power factor improvement). *For a given compensator  $Y_c$ . Power Factor Improvement is achieved if and only if*

$$(11) \quad PF > PF_u.$$

Following standard practice, we consider only lossless compensators, that is,

**Definition 6** (Lossless compensator). *The power-factor compensator is lossless if*

$$(12) \quad \langle Y_c(v_s), v_s \rangle = 0, \quad \forall v_s \in \mathcal{L}_2^n.$$

Furthermore, if  $Y_c$  is LTI, this is equivalent to

$$(13) \quad \text{Re}\{\hat{Y}_c(j\omega)\} = 0,$$

for all  $\omega \in \mathbb{R}$  for which  $j\omega$  is not a pole of  $\hat{Y}_c(j\omega)$ , where  $\text{Re}\{\hat{Y}_c(j\omega)\}$  is the real part of the admittance transfer matrix  $\hat{Y}_c(j\omega)$ .

The following results from [5] are repeated here for convenience to place our results in context.

**Proposition 3.** *Consider the system of Figure 2 with fixed  $Y_\ell$ . The compensator  $Y_c$  improves the PF if and only if the system is cyclodissipative with respect to the supply rate*

$$(14) \quad w(v_s, i_s) := (Y_\ell(v_s) + i_s)^\top (Y_\ell(v_s) - i_s).$$

The proof follows from (12) and the fact the compensator is lossless.

**Remark 2.** *Notice that we use scattering variables where the elements of  $\mathcal{U}$  denote the magnitude of the incident wave and those of  $\mathcal{Y}$  the magnitude of the reflected wave. The function  $w$  equals  $\|u\|^2 - \|y\|^2$ , i.e., the difference of the power in the incident wave and the reflected wave with  $u = i_\ell$  and  $y = i_s$ , where  $S$  still denotes the stored energy.*

The next result follows from Proposition 3 and it characterizes the set of all compensators  $Y_c$  that improve the power-factor for a given  $Y_\ell$ .

**Corollary 4.** *Consider the system of Figure 2 Then  $Y_c$  improves the PF for a given  $Y_\ell$  if and only if  $Y_c$  satisfies*

$$(15) \quad 2\langle Y_\ell(v_s), Y_c(v_s) \rangle + \|Y_c(v_s)\|^2 < 0, \quad \forall v_s \in \mathcal{L}_2^n.$$

Dually, given  $Y_c$ , the PF is improved for all  $Y_\ell$  that satisfy (15).

**Weighted power equalization renders into power factor improvement**

In this section we extend Proposition 5 in [5], where the PF compensators are assumed to be capacitors or inductors, to general lossless LTI filters.

We assume that the load is a nonlinear RLC circuit consisting of lumped dynamic elements ( $n_L$  inductors,  $n_C$  capacitors) and static elements ( $n_R$  resistors). Capacitors and inductors are defined by the physical laws and constitutive relations [12]:

$$(16) \quad i_C = \dot{q}_C, \quad v_C = \nabla H_C(q_C),$$

$$(17) \quad v_L = \dot{\phi}_L, \quad i_L = \nabla H_L(\phi_L),$$

respectively, where  $i_C, v_C, q_C \in \mathbb{R}^{n_C}$  are the capacitors currents, voltages and charges, and  $i_L, v_L, \phi_L \in \mathbb{R}^{n_L}$  are the inductors currents, voltages and flux-linkages,  $H_L : \mathbb{R}^{n_L} \rightarrow \mathbb{R}$  is the magnetic energy stored in the inductors,  $H_C : \mathbb{R}^{n_C} \rightarrow \mathbb{R}$  is the electric energy stored in the capacitors, and  $\nabla$  is the gradient operator. We assume that the energy functions are twice differentiable and for linear capacitors and inductors,  $H_C(q_C) = \frac{1}{2}q_C^\top C^{-1}q_C$ , and  $H_L(\phi_L) = \frac{1}{2}\phi_L^\top L^{-1}\phi_L$ , respectively, with  $L \in \mathbb{R}^{n_L \times n_L}$ ,  $C \in \mathbb{R}^{n_C \times n_C}$ . To avoid cluttering the notation we assume  $L, C$  are diagonal matrices.

Finally, we distinguish between two sets of nonlinear static resistors:  $n_{R_i}$  current-controlled resistors and  $n_{R_v}$  voltage-controlled resistors, for which the characteristics are given by the following one-to-one real-valued functions:

$$(18) \quad v_{R_i} = \hat{v}_{R_i}(i_{R_i}),$$

$$(19) \quad i_{R_v} = \hat{i}_{R_v}(v_{R_v}),$$

respectively, where  $i_{R_i}, v_{R_i} \in \mathbb{R}^{n_{R_i}}$  are the currents, voltages of the current-controlled resistors, and  $i_{R_v}, v_{R_v} \in \mathbb{R}^{n_{R_v}}$  are the currents, voltages of the voltage-controlled resistors, with  $n_R = n_{R_i} + n_{R_v}$ .

Recalling the definition of real power (9) we introduce the following.

**Definition 7.** (Weighted averaged power) *Given a compensator admittance  $Y_c$  the weighted averaged power (WAP) of a single-phase circuit with port variables  $(v, i) \in \mathcal{L}_2 \times \mathcal{L}_2$  is given by*

$$(20) \quad P^w := \langle Y_c(v), i \rangle.$$

If  $Y_c$  is LTI

$$(21) \quad P^w = \sum_{k=-\infty}^{\infty} \hat{Y}_c[k] \hat{V}[k] \hat{I}^*[k]$$

where  $\hat{V}[k], \hat{I}[k]$  are the  $k$ -th spectral lines of  $v$  and  $i$ , respectively, and  $\hat{Y}_c[k] := \hat{Y}_c(k\omega_0)$ , with  $\omega_0 := \frac{2\pi}{T}$ .

**Remark 3.** *WAP is the sum of the power components of the circuit modulated by the frequency response of  $Y_c$ —hence the use of the “weighted” qualifier. Moreover, since the spectral lines of real signals satisfy  $\hat{F}[-k] = \hat{F}^*[k]$ , the weighted power is a real number.*

The aforementioned definition motivates the next lemma.

**Lemma 5.** Consider a nonlinear time invariant (TI) current-controlled {voltage-controlled} one-port resistor characterized by (18) {(19)} and a fixed LTI lossless compensator  $Y_c$  with  $n = 1$ . Let  $\hat{Y}_c(j\omega)$  denote the associated admittance transfer function. If  $\hat{Y}_c(j\omega)$  has a zero at the origin, then the weighted averaged power, along periodic trajectories,

$$(22) \quad P_{R_i}^w := \langle Y_c v_{R_i}, i_{R_i} \rangle = 0,$$

{ $P_{R_v}^w := \langle Y_c v_{R_v}, i_{R_v} \rangle = 0$ } for all admissible pair  $(v_{R_i}, i_{R_i}) \in \mathcal{L}_2 \times \mathcal{L}_2$   $\{(v_{R_v}, i_{R_v}) \in \mathcal{L}_2 \times \mathcal{L}_2\}$ , and for all  $\omega \in \mathbb{R}$  for which  $j\omega$  is not a pole of  $\hat{Y}_c(j\omega)$ .

The proof is given in [14]. Lemma 5 suggests that the composition of an one-port resistor and a fixed one-port LTI defines a 1-port for which, in the case of periodic signals, the voltage and current are orthogonal to one another.

The following proposition gives the relation between WAP equalization and power-factor improvement by LTI lossless filters.

**Proposition 6.** Consider the system of Figure 2 with  $n = 1$ ,<sup>3</sup> a nonlinear RLC load, with linear resistors, and a fixed LTI lossless compensator  $Y_c$  with admittance transfer function  $\hat{Y}_c(s)$ .

i) PF is improved if and only if

$$(23) \quad \frac{1}{2}V_s^w + \sum_{q=1}^{n_L} P_{L_q}^w + \sum_{q=1}^{n_C} P_{C_q}^w < 0$$

where  $V_s^w$  is the rms value of the filtered voltage source, that is,

$$V_s^w := \|\hat{Y}_c v_s\|^2 = \sum_{k=1}^{\infty} |\hat{Y}_c(k) \hat{V}_s(k)|^2,$$

and

$$P_{C_q}^w := \sum_{k=-\infty}^{\infty} \hat{Y}_c[k] \hat{V}_{C_q}[k] \hat{I}_{C_q}^*[k]$$

$$P_{L_q}^w := \sum_{k=-\infty}^{\infty} \hat{Y}_c[k] \hat{V}_{L_q}[k] \hat{I}_{L_q}^*[k],$$

are the weighted powers of the  $q$ -th capacitor and inductor, respectively.

ii) Condition (23) may be equivalently expressed as

$$(24) \quad \left\langle \left( \frac{1}{p} Y_c \right) v_L, \nabla^2 H_L v_L \right\rangle - \left\langle i_C, \left( \frac{1}{p} Y_c \right) \nabla^2 H_C i_C \right\rangle > \frac{1}{2} V_s^w$$

where  $p := \frac{d}{dt}$ .

iii) If the capacitors and inductors are linear their weighted powers become

$$(25) P_{C_q}^w := 2\omega_0 \sum_{k=1}^{\infty} \left\{ k \operatorname{Im}\{\hat{Y}_c[k]\} \sum_{q=1}^{n_C} C_q |\hat{V}_{C_q}[k]|^2 \right\}$$

$$P_{L_q}^w := -2\omega_0 \sum_{k=1}^{\infty} \left\{ k \operatorname{Im}\{\hat{Y}_c[k]\} \sum_{q=1}^{n_L} L_q |\hat{I}_{L_q}[k]|^2 \right\}.$$

where  $\operatorname{Im}\{\hat{Y}_c[k]\}$  is the imaginary part of the admittance transfer function  $\hat{Y}_c[k]$ .

iv) Furthermore, the results i-iii can be extended for load

<sup>3</sup>This condition is imposed, without loss of generality, to simplify the presentation of the result.

with nonlinear TI resistors, if the admittance transfer function  $\hat{Y}(j\omega)$  of the LTI lossless compensator has a zero at the origin.

The proof, which follows from Lemma 5 and the generalized form of Tellegen's theorem [13], is given in complete detail in [14].

**Remark 4.** Condition (23) indicates that the PF will be improved if and only if the overall weighted power (supplied plus stored) is negative.

**Remark 5.** From (24) (or replacing (25) in (23)) we see that PF improvement is equivalent to average power equalization between inductors and capacitor—notice the minus signs—with the gap being determined by the weighted supplied power.

### Application of the framework

In this section, we focus our attention to the PFC of LTI loads by the use of a passive filter.

Consider an one-port  $\mathfrak{N}$  with linear passive resistors, inductors, and capacitors supplied by a periodic nonsinusoidal voltage source  $v_s(t)$ . Considering periodicity, we can express the voltage source in terms of its (exponential) Fourier series as  $v_s(t) = \sum_{k=-\infty}^{\infty} \hat{V}_s(k) \exp(jk\omega_0 t)$ , where  $\omega_0 := 2\pi/T$  is the fundamental frequency and, for integers  $k$ ,  $\hat{V}_s(k) := \frac{1}{T} \int_0^T v_s(t) \exp(-jk\omega_0 t) dt$  are the Fourier coefficients of the voltage source, also called spectral line or harmonics.

The LTI load  $Y_\ell$  can be characterized in terms of the associated admittance, specified by its conductance  $G[k]$  and susceptance  $B[k]$  for the  $k$ th order harmonic, namely  $\hat{Y}_\ell[k] = G[k] + jB[k]$ . Additionally, it can be expressed in terms of the magnetic and electric energies of the  $k$ th harmonic as

$$(26) \quad \hat{Y}_\ell[k] = \frac{2P(k\omega_0)}{|\hat{V}_s[k]|^2} + j \frac{k\omega_0}{|\hat{V}_s[k]|^2} \left\{ \sum_{q=1}^{n_C} C_q |\hat{V}_{C_q}[k]|^2 - \sum_{q=1}^{n_L} L_q |\hat{I}_{L_q}[k]|^2 \right\}$$

where  $P(k\omega_0) = \frac{1}{2} \sum_{p=1}^{n_R} G_{R_p}[k] |\hat{V}_{R_p}[k]|^2$ ,  $\hat{V}[k]$  is the  $k$ th spectral line of  $v_s(t)$ , and  $\hat{V}_{C_q}[k]$ ,  $\hat{I}_{L_q}[k]$  are the spectral lines of the  $q$ th capacitor voltage and inductor current.

From the Proposition 6, for LTI loads and a given compensator  $Y_c$ , we have that  $PF > PF_u$  if and only if the following inequality verify

$$\sum_{q=1}^{n_L} P_{L_q}^w + \sum_{q=1}^{n_C} P_{C_q}^w + \frac{1}{2} V_s^w < 0$$

or, by Claim 3 of the proposition,

$$2\omega_0 \sum_{k=1}^{\infty} k \operatorname{Im}\{\hat{Y}_c[k]\} \left\{ \sum_{q=1}^{n_C} C_q |\hat{V}_{C_q}[k]|^2 - \sum_{q=1}^{n_L} L_q |\hat{I}_{L_q}[k]|^2 \right\} + \sum_{k=1}^{\infty} |\hat{Y}_c[k] \hat{V}_s[k]|^2 > 0$$

and, in comparison with (26), we obtain the following

$$(27) \quad \sum_{k=1}^{\infty} |\hat{Y}_c[k] \hat{V}_s[k]|^2 + 2\omega_0 \sum_{k=1}^{\infty} k \operatorname{Im}\{\hat{Y}_c[k]\} B[k] |\hat{V}_s[k]|^2 < 0.$$

### A. Maximum power factor for LIT load by capacitive compensator

A typical industrial approach for maximizing PF is sought for the connection of a variable, lossless capacitor  $C$  in par-

allel with the load. The capacitive compensation in systems with nonsinusoidal voltages and currents has been widely studied, for instance [1, 2, 3] and [4]. In a similar way, consider that the compensator as  $\hat{Y}_c(s) = sC$  in (27). Furthermore, if power-factor improvement is possible, the optimal value of the capacitance is obtained as follow. So, from (27) we define the function:

$$(28) \quad f(C) = C^2 \sum_{k=-\infty}^{\infty} (k\omega_0)^2 |\hat{V}_s[k]|^2 + 2\omega_0 C \sum_{k=-\infty}^{\infty} kB[k] |\hat{V}_s[k]|^2.$$

The problem becomes on optimization problem, i.e., the optimal values of the unknown  $C$  that minimize the right side of the inequality (27) which has the minimizer

$$(29) \quad C_{opt} = \frac{-\sum_{k=-\infty}^{+\infty} k\omega_0 B[k] |\hat{V}_s[k]|^2}{\sum_{k=-\infty}^{+\infty} k^2 \omega_0^2 |\hat{V}_s[k]|^2}.$$

This result is also obtained in [5] and for  $v_s(t) = V_s \sin \omega_0 t$  in [3].

However, the capacitive compensation may not be effective for distorted voltage, [4]. Therefore, alternative circuit topologies are studied in the circuits literature [2] but there seem to be many open problems for this solution. For instance, in [4] and [15], it is shown that for RL loads the optimal solution for parallel shunt LC compensator corresponds to a negative inductance, and thus a switched series LC circuits is suggested as an alternative solution. Furthermore, as it is shown in [3], if the supply bus has not an infinite power then a shunt reactive compensator changes the load voltage. This change could be particularly high when the resonance between the compensator and the supply source occurs in the systems. Indeed, usually it is the resonance between capacitance of the compensator and the inductance of the supply source. Moreover, in the practical applications most of the loads present impedance RL type, which increases with the frequency. Consequently, a circuit with a such load supplied from an inductive source and compensated by a capacitor behaves in the range of frequency  $\omega \gg \omega_0$ , see Fig. 3.

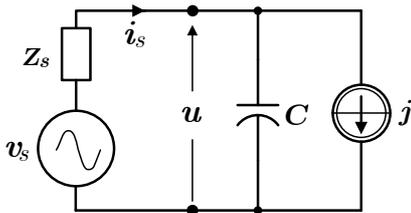


Fig. 3. Equivalent circuit of compensated load for  $\omega \gg \omega_1$ , where a current source  $j$  represents the harmonics generated by the load and  $Z_s = j\omega L_s$  is the inductive impedance of the source.

The Load to Distribution Voltage (LVD) transmittance, which expresses dependence of the load voltage on frequency in the circuit with a voltage resonance, is given by

$$\frac{U(j\omega)}{V_s(j\omega)} = \frac{1}{1 - \omega^2 L_s C} = \frac{1}{1 - \left(\frac{\omega}{\omega_r}\right)^2}$$

where  $\omega_r = \frac{1}{\sqrt{L_s C}}$  is the resonant frequency. The bode plot of the magnitude of the LVD transmittance  $A(j\omega)$  is shown in Fig. 4. We observe that the magnitude of LVD transmittance is higher than zero for frequency  $\omega$  such  $(\omega/\omega_r)^2 < 2$ . It means that all distribution voltage harmonics of the fre-

quency below  $\sqrt{2}\omega_r$  are amplified by the compensator. This amplification increases to infinity at resonant frequency.

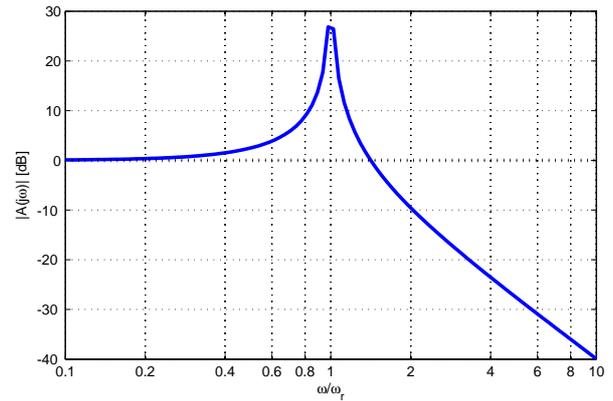


Fig. 4. Bode plot of L/DV transmittance versus relative frequency  $\omega/\omega_r$

From the above shown, previously pointed out in [3], the capacitive compensation in systems with nonsinusoidal voltages and currents could contribute to an increase of voltage and current distortion and have low effectiveness. In addition, a possible total compensation of reactive power usually requires a compensator built of a high number of reactive components. Hence, such requirement could be fulfilled by the LC compensator shown in Fig. 5. Furthermore, a series LC compensator does not increase waveform distortion and it can be assumed that the voltage harmonics are not affected by the compensator if  $L$  and  $C$  are selected such that frequency range of amplification is below the 2<sup>nd</sup>, i.e.,

$$\frac{1}{\sqrt{(L_s + L)C}} < \sqrt{2}\omega_0,$$

where  $\omega_0$  is fundamental frequency.

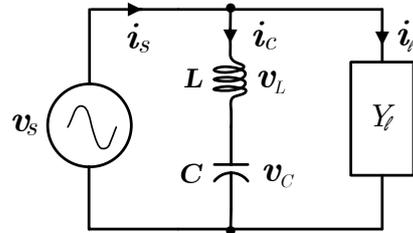


Fig. 5. Series LC-Compensation of Linear Network.

## B. Maximum power factor for LIT load by series LC compensator

In [5] and [16] the PF improvement by cyclodissipativity condition was presented, but the improvement had been carried out by either capacitive or inductive compensation. Now, we consider for the power factor improvement of a linear load by a series LC compensator based on cyclodissipativity, see Fig. 5. We outline the calculations. By the Corollary 7 we have that series LC compensation improves the power factor,  $PF > PF_u$ , if and only if

$$0 > \sum_{k=-\infty}^{\infty} \left( \frac{k\omega_0 C}{1 - k^2 \omega_0^2 LC} \right)^2 |\hat{V}_s[k]|^2 + 2 \sum_{k=-\infty}^{\infty} \frac{k\omega_0 C}{1 - k^2 \omega_0^2 LC} B(k\omega_0) |\hat{V}_s[k]|^2$$

where, the admittance transfer function of the compensator is given by

$$\hat{Y}_c(j\omega) = \frac{j\omega C}{1 - \omega^2 LC},$$

and  $C$  and  $L$  are the capacitance and inductance, resp.

In order to find the optimal values of the unknown  $C$  and  $L$ , we define the function

$$(30) \quad f(L, C) = \sum_{k=-\infty}^{\infty} \left( \frac{k\omega_0 C}{1 - k^2 \omega_0^2 LC} \right)^2 |\hat{V}_s[k]|^2 + 2 \sum_{k=-\infty}^{\infty} \frac{n\omega_0 C}{1 - k^2 \omega_0^2 LC} B[k] |\hat{V}_s[k]|^2.$$

By assuming that  $L_s \ll L$ . The series inductance  $L$  must be selected such that the frequency range of amplification is below the 2<sup>nd</sup> order harmonic, i.e.  $\frac{1}{\sqrt{LC}} < \sqrt{2}\omega_0$ , with  $\omega_0$  is the fundamental frequency. Hence we define the following constraint

$$(31) \quad g(L, C) = \frac{1}{\sqrt{LC}} - \sqrt{\alpha}\omega_0 = 0.$$

with  $0 < \alpha < 2$  and  $k \neq 1$ .

Therefore, the problem becomes to find the local critical points of  $f$  restricted to  $S$ , and let  $S$  be the level set for  $g$  with value 0. By the method of Lagrange Multiplier and considering  $c = 0$  we have  $f$  restricted to  $S$  which has a local minimum on  $S$  at

$$(32) \quad C_{opt} = \frac{\sum_{k=-\infty}^{\infty} \frac{n\omega_0}{1 - \frac{k^2}{\alpha}} \frac{X(k\omega_0)}{R^2(k\omega_0) + X^2(k\omega_0)} |\hat{V}_s[k]|^2}{\sum_{k=-\infty}^{\infty} \frac{k^2 \omega_0^2}{(1 - \frac{k^2}{\alpha})^2} |\hat{V}_s[k]|^2}$$

and, using the constrain  $g(L, C) = 1/\sqrt{LC} - \sqrt{\alpha}\omega_0 = 0$ ,

$$(33) \quad L_{opt} = \frac{1}{\alpha \omega_0^2 C_{opt}},$$

where the susceptance  $B(k\omega_0)$  is given by

$$B[k] = \frac{-X(k\omega_0)}{R^2(k\omega_0) + X^2(k\omega_0)}.$$

An analogous result can be found for parallel LC compensators. We now apply our result to an example system, [1].

**Example 1.** A periodic nonsinusoidal voltage given by  $v_s = \sqrt{2}[200 \sin \omega_0 t + 200 \sin(5\omega_0 t + 30^\circ)]$  is applied to a series, linear, resistance-inductance load with resistance 4  $\Omega$  and fundamental frequency reactance 10  $\Omega$ . Consider the fundamental frequency as  $f_1 = 50$  Hz.

The uncompensated circuit has the power factor  $PF_u = 0.27$ . From (29), the optimum capacitance that will give maximum power factor has the value  $C_{opt} = 22.8 \mu F$ . The Power Factor after compensation is  $PF = 0.292$ . This result is the same as in [1]. The degree of the power factor improvement from 0.27 to 0.292 is seen to be very small. This is typical for circuits in which the voltage is grossly nonsinusoidal, see [1] for more cases. The optimal capacitor and inductor given by LC compensation, e.i. (32) and (33), resp., are  $C_{opt} = 124.4 \mu F$  and  $L_{opt} = 40.7 mH$ , yielding an improved power factor  $PF = 0.4987$ , with  $k = 1.999$ . The power factor can be increased to a value which is approximately twice as high as the uncompensated power factor.

## Conclusions

In this paper, extensions to the analysis of power factor compensation of nonsinusoidal networks based on cyclo-dissipativity were presented. We have studied the concept of cyclo-dissipativity property of electrical circuits and showed that

the power factor by general LTI compensators is improved if and only if a certain equalization condition between the weighted powers of compensator and load is ensured. Based on this condition, we have given criteria for improvement of PF with linear capacitors and LC filters. These criteria determine the optimal values for the compensator parameters. The analysis is carried out for the general periodic and nonsinusoidal supply voltage. Although we only study the linear case, this can be done for a nonlinear one as well.

## REFERENCES

- [1] Shepherd W., Zand P.: *Energy Flow and Power Factor in Non-sinusoidal Circuits*. Cambridge University Press, 1979.
- [2] Shepherd W., Zakikhani W.: Power factor correction in nonsinusoidal systems by the use of capacitance, *J. Phys. D: Appl. Phys.*, vol. 6, pp. 1850–1861, 1973.
- [3] Czarnecki L.S.: Currents' physical components in circuits with nonsinusoidal voltages and currents. part 1: Single-phase linear circuits, *Journal of Electrical Power Quality and Utilization*, vol. XI, no. 2, pp. 3–14, 2005.
- [4] Czarnecki L.S.: A time-domain approach to reactive current minimization in nonsinusoidal situations, *IEEE Transactions on Instrumentation and Measurement*, vol. 39, pp. 698–703, Oct 1990.
- [5] Garcia-Canseco E., Grino R., Ortega R., Salichs M., Stankovic A.: Power-factor compensation of electrical circuits: a framework for analysis and design in the nonlinear nonsinusoidal case, *IEEE Control Systems Magazine*, 27, pp.46-59, April 2007.
- [6] Willems J.C.: Qualitative behaviour of interconnected systems, *Ann. Systems Res.*, 3, pp. 61-80, 1973.
- [7] Hill D.J., Moylan P.J.: Dissipative dynamical systems: Basic input-output and state properties, *The Journal of the Franklin Institute*, 309, pp. 327-357, May 1990.
- [8] Czarnecki L.S.: Budeanu and fryze: Two frameworks for interpreting power properties of circuits with nonsinusoidal voltages and currents, *Electrical Engineering*, 80, pp. 359-367, 1997.
- [9] Czarnecki L.S.: Energy flow and power phenomena in electrical circuits: illusion and reality, *Electrical Engineering*, 82, pp.119-126, 2000.
- [10] del Puerto-Flores D., Ortega R., Scherpen J.M.A.: Power factor compensation with lossless linear filters is equivalent to (weighted) power equalization and a new cyclo-dissipativity characterization, *Conference on Decision and Control*, 2009, Dec 2009.
- [11] Definitions for the Measurement of Electric Quantities Under Sinusoidal, Nonsinusoidal, Balance, or Unbalance Conditions. *IEEE Std 1450-2000*, March 2010.
- [12] Chua L.O., Desoer C. A., Kuh E. S.: *Linear and Non-Linear Circuits*. McGraw-Hill College, 1987.
- [13] Penfield P., Spence R., Duinker S.: A generalized form of Tellegen's theorem, *IEEE Transactions on Circuit Theory*, 17, pp. 302-305, Aug. 1970.
- [14] del Puerto-Flores D., Scherpen J.M.A., Ortega R.: On power factor improvement by lossless linear filters in the nonlinear nonsinusoidal case, *International School on Nonsinusoidal Currents and Compensation (ISNCC)*, 2010, pp. 270-212, Jun 15-18, 2010.
- [15] Page, C.H.: Reactive power in nonsinusoidal situations, *IEEE Transactions on Instrumentation and Measurement*, vol. 29, pp. 420–423, Dec. 1980.
- [16] Ortega R., Hernandez-Gomez M., Lamnabhi-Lagarrigue F., Escobar G.: Passive power factor compensation of a controlled rectifier with non-sinusoidal generator voltage, *Conference on Decision and Control*, 2008, pp. 3755-3760, Dec 2008.

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