

Design and Analyses of Reconfigurable Dumbbell-Shaped and Modified H-Shaped DGSs in Millimeter Wave Band

Abstract. The design and analyses of reconfigurable dumbbell-shaped and modified H-shaped Defected Ground Structures (DGS) in the millimeter wave band (26/28 GHz band) are presented in this paper. The proposed DGSs were designed to be a reconfigurable between bandstop and allpass responses that would be used for 5G interference mitigation and as well as in RF switch design. In the design process, a mathematical model was developed for the analysis of reconfigurable capability between these two responses. Then, based on the E and H field concentration in the electromagnetic (EM) simulation, a suitable location of a PIN diode for the electronically controlled between bandstop and allpass was physically identified on the proposed reconfigurable DGS. Finally, a preliminary verification with an ideal PIN diode (open and short circuited) on the fabricated design was validated with the simulation results. Results showed that the proposed reconfigurable DGS can be changed between bandstop and allpass. The attenuation of the bandstop was more than -20 dB and the insertion loss of the allpass was lower than -3 dB in the 26/28 GHz band.

Streszczenie. W artykule przedstawiono projektowanie i analizy rekonfigurowalnych i zmodyfikowanych struktur naziemnych w kształcie hantli (DGS) w paśmie fal milimetrowych (pasmo 26/28 GHz). Proponowane systemy gwarancji depozytów zostały zaprojektowane tak, aby były rekonfigurowalne między pasmowym zatrzymaniem a wszystkimi odpowiedziami przejścia, które byłyby wykorzystywane do łagodzenia zakłóceń 5G, a także do projektowania przełączników RF. W procesie projektowania opracowano model matematyczny do analizy rekonfigurowalnych możliwości między tymi dwiema odpowiedziami. Następnie, w oparciu o stężenie pola E i H w symulacji elektromagnetycznej (EM), odpowiednia lokalizacja diody PIN dla elektronicznie sterowanego między pasmowym ogranicznikiem a wszystkimi przejściami została fizycznie zidentyfikowana na proponowanym rekonfigurowalnym DGS. Na koniec wstępna weryfikacja z idealną diodą PIN (otwartą i zwartą) na wytworzonej konstrukcji została zweryfikowana wynikami symulacji. Wyniki pokazały, że proponowany rekonfigurowalny system DGS można zmieniać między bandstopem a all pass. Tłumienie ogranicznika pasma było większe niż 20 dB, a tłumienie wtrąceniowe wszystkich przebiegów było mniejsze niż -3 dB w paśmie 26/28 GHz. (Projektowanie i analizy rekonfigurowalnych systemów DGS w kształcie hantli i zmodyfikowanych systemów DGS w kształcie litery H w paśmie fal milimetrowych)

Keywords: defected ground structure; millimeter wave; reconfigurable DGS.

Słowa kluczowe: uszkodzona struktura gruntu; fala milimetrowa; rekonfigurowalny system DGS.

Introduction

Defected ground structure (DGS) is popularly used in high frequency (radio frequency spectrum) such as filters [1-4], antenna [5-7], power amplifier [8-10], switches [11], and power divider [12-14]. The use of the DGS in these circuits has its different purposes such as isolation [6, 9], load matching network [8], miniaturization [2, 12, 13], harmonics or frequency suppression [2, 10, 12], and wideband performance [5, 9, 14].

On the other hand, in designing a wireless communication system, engineers have always had to worry about interference from both outside sources and other users of the wireless technology. In order to accurately recover the sent information, the classical wireless communications design cycle has included monitoring or forecasting channel impairments, selecting a modulation strategy, signal preconditioning at the transmitter, and processing at the receiver. Therefore, several interferences in wireless communication have been discussed such as WLAN [15], Bluetooth [16] and fifth generation (5G) [17].

Currently, 5G millimetre wave technologies have taken centre stage in the growth of the information and telecommunications industry. The need for increased data rates and the allocation of a spectrum band at higher frequencies (in millimeter wave) have led to the development of complicated and dynamic wireless communication systems [18]. As reported in [19], it was demonstrated that the fixed-satellite service (FSS) experiences interference from numerous IMT-2020s (5G communication standard) in the millimetre wave band. Therefore, this new implementation will conflict with the existing technologies.

To reduce any potential interference, a cognitive radio (CR) is required in 5G millimetre wave communications. As a result, there are several techniques of interference mitigation in CR systems, including multi-hop multiple input multiple output (MIMO) decode-and-forward relaying protocol [20], spectrum sharing [21, 22], interference mitigation in ultra-wideband (UWB) receiver architecture by exploiting the spatial and spectral diversity [23], user clustering and resource allocation in NOMA [24], cooperative spectrum sensing [25], interference management technique in device-to-device (D2D) applications [26, 27]. In addition, reconfigurable circuit components such as filters [17] and antennas [7, 28, 29] are required in front-end CR systems in order to support multichannel, multiband, and multifunctional operations while also offering an interference mitigation solution. To physically suppress interference in the front-end receiver, these techniques are known as an active interference cancellation [23]. Therefore, as mentioned in [17, 28], device controls utilizing PIN or varactor diodes are necessary.

In the reconfigurable or switchable filtering methods that can be applied in a CR system, the filter response can be changed from bandpass to bandstop [30-33] and from bandstop to allpass [34-36]. A variety of resonators such as T-type inductive coupling structure [32], microstrip line lossy resonators [33], evanescent-mode cavity resonator [34], substrate integrated waveguide (SIW) resonator [35] and acoustic-wave-lumped-element resonator [36] were implemented in the switchable/reconfigurable filter designs.

In this paper, two types of defected ground structure (DGS) which are dumbbell-shaped and modified H-shaped were designed to be a reconfigurable between bandstop

and allpass responses in the millimeter wave band (26/28 GHz band). The proposed DGSs would be used for 5G interference mitigation in CR systems. In the design process, a mathematical model was developed for the analysis of reconfigurable capability between these two responses. Then, an EM simulation was performed in order to physically identify a suitable location of the PIN diode for the electronically controlled between bandstop and allpass. This is based on the E and H field concentration on the proposed reconfigurable DGS. Finally, a preliminary verification with an ideal PIN diode (open and short circuited) on the fabricated design was carried out for validating between measurement and simulation results.

Besides, in the near future, the proposed designs have a potential to be used in RF switch design for wireless communications such as 4G and 5G as discussed in [11, 37]. This is the fact that, the proposed reconfigurable DGS is the same concept of using any switchable resonator in RF switch design as reported in [38-40].

Mathematical Analysis of a Reconfigurable DGS

In this section, a reconfigurable DGS model as shown in Figure 2 is discussed via a mathematical analysis. The analysis is based on two conditions: bandstop and allpass responses. The reconfigurable DGS model is an equivalent circuit of DGS (L and C) using a PIN diode (D), that can be switched between bandstop and allpass responses.

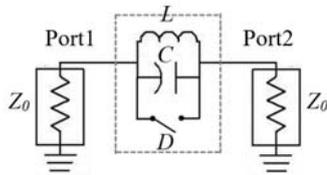


Fig. 1. Reconfigurable DGS equivalent circuit of parallel L and C with PIN diode switch.

The equivalent circuit of DGS is a simple parallel L and C . Thus, the DGS impedance is calculated as

$$(1) \quad Z_{DGS} = jX_{LC} = \frac{\left(\frac{1}{j\omega C}\right)(j\omega L)}{\left(\frac{1}{j\omega C}\right) + (j\omega L)} = \frac{j\omega L}{1 + \omega^2 LC}$$

Now, consider the PIN diode in OFF state, which ideally be an open circuit of the DGS. As a result, the DGS will act as a bandstop response. Then, the DGS's transmission matrix ($ABCD$) is

$$(2) \quad T_{DGS} = \begin{bmatrix} 1 & Z_{DGS} \\ 0 & 1 \end{bmatrix} = \begin{bmatrix} 1 & \left(\frac{j\omega L}{1 + \omega^2 LC}\right) \\ 0 & 1 \end{bmatrix}$$

The bandstop response of S_{21} is then derived from (2) using conversion between $ABCD$ and S -parameter to

$$(3) \quad S_{21} = \frac{2}{A + \frac{B}{Z_0} + Z_0 C + D} = \frac{2}{2 + \frac{j\omega L}{(1 - \omega^2 LC)Z_0} + Z_0}$$

where Z_0 is a characteristic impedance of the input and output of the DGS. Referring to (3), if $Z_0 = 1$ which is a normalize impedance, L and C will produce a bandstop response. Therefore, a resonant frequency is produced when

$$(4) \quad j\omega L - \frac{j}{\omega C} = 0.$$

Thus,

$$(5) \quad f_0 = \frac{1}{2\pi LC}$$

where f_0 is the resonant frequency in Hertz (Hz).

The following allpass response analysis is considered at the PIN diode in an ON state. In this analysis, the DGS should ideally be short circuited. As a result, the DGS responds as an allpass response. Theoretically, the allpass response can be produced if at least one or both of the components of L and C are zero. Therefore, if C is removed by short circuited of the PIN diode, C will become zero. Then let $Z_0 = 1$, which is a normalized impedance, thus S_{21} of (3) becomes

$$(6) \quad S_{21} = \frac{2}{2 + \frac{j\omega L}{(1 - \omega^2 L(0))} + 1} = \frac{2}{3} \approx 1$$

or in decibel (dB), which is

$$(7) \quad |S_{21}|^2 dB = 20 \log_{10}(1) = 0 \text{ dB}.$$

From (7), it is clear that an ideal zero insertion loss can be obtained, producing an allpass response.

Based on the mathematical analyses, additional analysis in electronic design automation (EDA) software (for instance, CST Microwave Studio) is required to the proposed DGS in order to make it electronically reconfigurable between bandstop and allpass responses. Identification of the effective location of the PIN diode on the DGS is needed in the EDA software. This analysis is important so that it will tally with the mathematical analysis of the equivalent circuit of the reconfigurable DGSs.

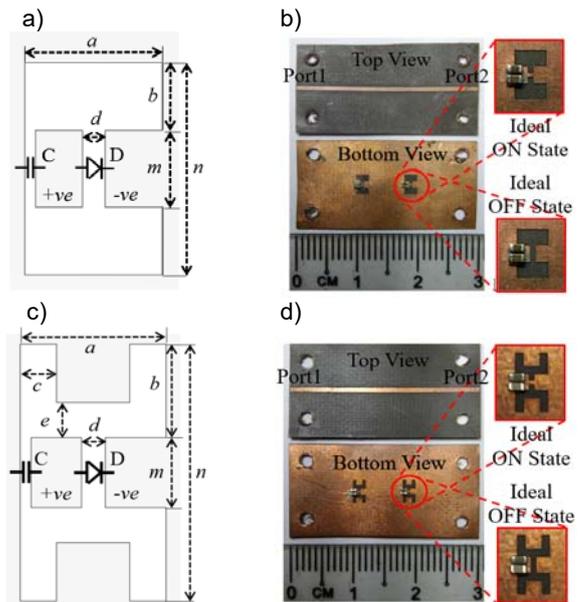


Fig. 2. Dimensions and the fabricated DGSs for (a) Design A: dimension, (b) Design A: prototype, (c) Design B: dimension, (d) Design B: prototype.

Reconfigurable DGS Design for Dumbbell-Shaped and Modified H-Shaped

To design the reconfigurable DGS circuit, the concept of an ideal switch is used. Each DGS is presented by two layouts, which are open circuited condition (active DGS) and short circuited condition (inactive DGS). The active DGS produces bandstop response due to OFF state of the PIN diode and the inactive DGS produces allpass response due to ON state of the PIN diode. Figure 2(a) shows the dimensions of the dumbbell-shaped DGS (denoted as Design A) and Figure 2(b) shows the fabricated prototype of the DGS. Figure 2(c) and Figure 2(d) show the dimensions of the modified H-shaped DGS (denoted as Design B) and the photograph of the fabricated DGS, respectively. A summary of the dimensions is shown in Table 1.

As can be seen in the Figure 2(a) and 2(c), the location of the PIN diode in the DGSs was identified based on the EM simulation for the E and H field. This simulation results are discussed in the Results and Discussion section. Besides, capacitor (C) is also used as a DC block to create a small positive (+ve) area of voltage supply to the anode of the PIN diode (D).

For validation, Design A and B were fabricated using Roger RT/Duroid 5880 with 0.254 mm thickness and relative dielectric constant of 2.2. Each prototype (the bottom view) consists of a cascaded two DGSs with 6 mm distance to provide a higher bandstop response. Therefore, the overall dimension of the fabricated design is 30 mm x 15 mm = 450 mm². The top view is a microstrip line with 50 Ω impedance in 26/28 GHz band. The insertion loss, attenuation and return loss performances of the fabricated DGSs were measured using the Microwave Network Analyzer which connected to a high precision cable.

Table 1. Dimension of Design A and Design B

Parameters	Dimensions [mm]	
	Design A	Design B
<i>a</i>	2	2
<i>b</i>	1	1.34
<i>c</i>	-	0.5
<i>d</i>	0.3302	0.3302
<i>e</i>	-	0.5
<i>m</i>	1.13	1
<i>n</i>	3.13	3.68

Results and Discussion

CST Microwave Studio was used to simulate the electromagnetic (EM) field distribution of Design A and Design B at the centre frequency of 27 GHz in the 26/28 GHz band. Figure 3(a) is the E-field concentration for Design A (left) and Design B (right). Meanwhile, Figure 3(b) is the H-field concentration for Design A (left) and Design B (right).

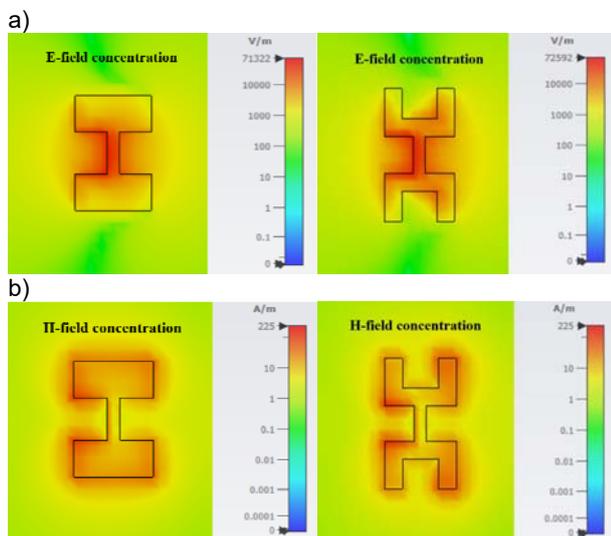


Fig. 3. Simulated EM field distribution of the reconfigurable DGSs for the concentration of (a) E-field and (b) H-field.

It was found that the concentration of E-field was mainly located in the middle of the structure of Design A and B. Meanwhile, the H-field was concentrated almost equally surrounding of the structures. Take note that the E-field is related to the capacitance and the H-field is related to the inductance of the DGSs. Thus, if referring to the mathematical analysis, the most practical to reconfigure between bandstop and allpass is by disturbing the E-field of the DGS which is related to the capacitor element. This can

be referred to (6) and (7). Therefore, a PIN diode should be put in the middle of the DGS with a suitable *d*, that is not longer than the length of the PIN diode package.

Figure 4 (a) and (b) show the simulated and measured results of the reconfigurable DGS for both Design A and Design B during the active DGS (which is an ideal OFF state of the PIN diode). The results produced a bandstop response with more than 20 dB of attenuation (S₂₁) in the 26/28 GHz band. Comparing between Design A and B, it can be seen that Design A is almost comparable between simulation and measurement results. Besides, it showed that the maximum notch (simulation) was at 26.8 GHz for Design A and 29.1 GHz for Design B, respectively. Meanwhile, the return loss (S₁₁) was very close to the 0 dB.

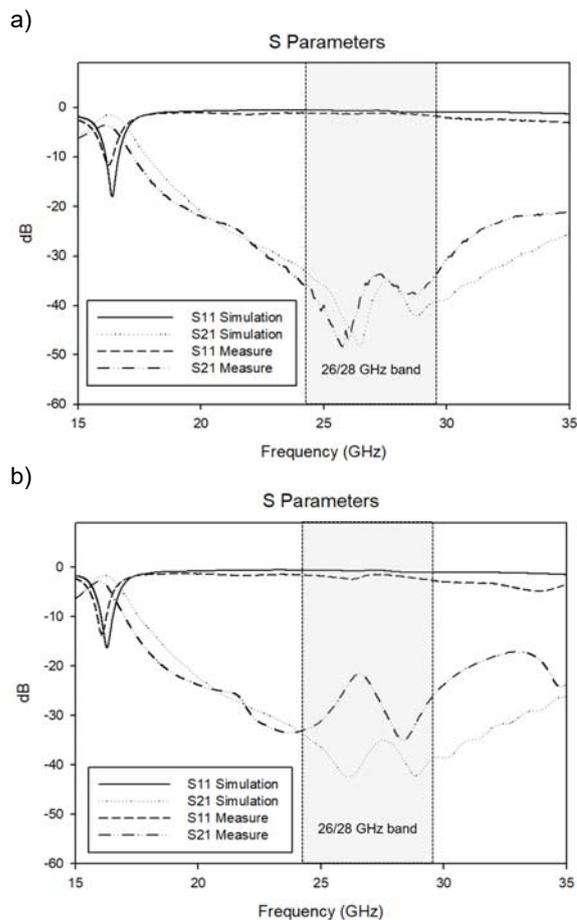
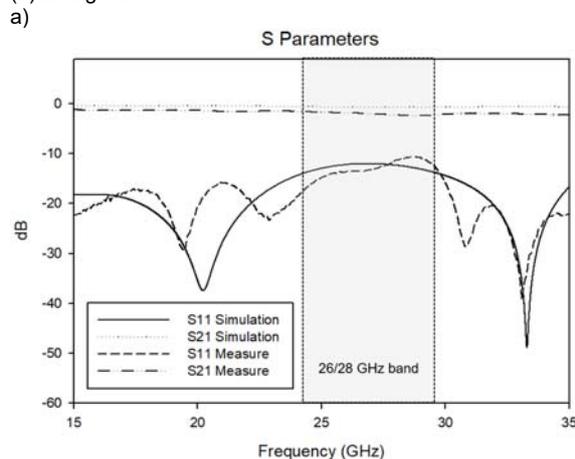


Fig. 4. Bandstop response of reconfigurable DGSs for (a) Design A (b) Design B.



b)

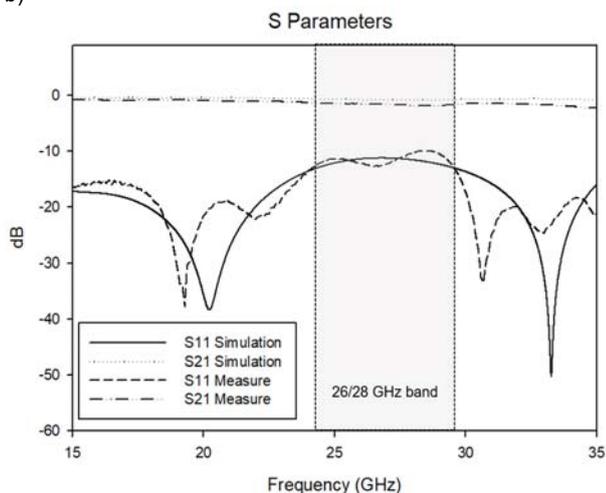


Fig. 5. Allpass response of reconfigurable DGSs for (a) Design A (b) Design B.

The simulated and measured results of the reconfigurable DGS during the inactive DGS (which is an ideal ON state of the PIN diode) are shown in Figure 5 (a) and (b). The results produced an allpass response with less than 3 dB of insertion loss (S21) in the 26/28 GHz band. Meanwhile the return loss (S11) was higher than -10 dB across the band. The measurement and simulation results are almost comparable for both Design A and B.

Table 2 below shows the summarized of the performance comparison between measurement and simulation results for the Design A and B. In general, all the measured results of design A and B were tallied and comparable with the simulated results, except for the bandstop response (attenuation) for Design B (the difference with 13.49 dB). This could be due to the fabrication or the assembly of the design. The next step for the proposed reconfigurable DGS is to implement the actual PIN diode with a biasing circuit for the ON and OFF state operations. The PIN diode is a package type that can be soldered on the printed circuit board of the DGS.

Table 2. Insertion loss, attenuation and return loss performances at 27 GHz (center of 26/28 GHz band)

DGS Design		Allpass Response (ON state)		Bandpass Response (OFF state)	
		Insertion Loss (S21 in dB)	Return Loss (S11 in dB)	Attenuation (S21 in dB)	Return Loss (S11 in dB)
Design A	Simulation	0.69	11.93	39.31	0.63
	Measured	1.98	13.30	33.92	1.19
Design B	Simulation	0.78	11.25	37.02	0.67
	Measured	1.60	12.22	23.53	1.55

Conclusion

The proposed reconfigurable DGSs using dumbbell-shaped and modified H-shaped for millimeter wave in 26/28 GHz were designed and a simple mathematical modelling was discussed for the bandstop and allpass responses. Based on the mathematical analysis, the DGS can be switched to allpass if at least one or both of the components of L and C are zero. Then, the simulated E-field and H-field were analyzed and, it was found that the PIN diode location can be determined by referring to the maximum E-field concentration compared to the H-field. Finally, the proposed reconfigurable DGSs were fabricated using the Rogers RT/Duroid 5880 material and the measured results were tallied with simulated results and were performed based on the ideal ON and OFF states of the PIN diode. These designs could be used in cognitive radio systems for the 5G interference mitigation and as well as in RF switch design.

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